

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,965,653 B2
APPLICATION NO. : 09/746277
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INVENTOR(S) : William Eric Main et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8, Line 32, Claim No. 14:

Change "terminal of the eleventh transistor and an second" to --terminal of the eleventh transistor and a second--

Column 8, Line 36, Claim No. 15:

Change "an thirteenth transistor having a control terminal coupled" to --a thirteenth transistor having a control terminal coupled--

Column 8, Line 38, Claim No. 15:

Change "terminal coupled to the first power conductor;" to --terminal coupled to the first power supply conductor;--

Signed and Sealed this

Twenty-ninth Day of April, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office